

THAT WHICH IS CLAIMED IS:

1. A comparator with two thresholds comprising a two-threshold latch of which one input and one output respectively form an input and an output of the comparator, said latch also comprising a first midpoint between a first power supply terminal and the output of the latch, the comparator also comprising a first negative feedback loop acting on the first midpoint to set the first threshold of the comparator as a function of a first power supply potential,

wherein the first threshold is also a function of a first reference potential that is stable.

2. A comparator according to claim 1, wherein the latch also comprises a second midpoint between a second power supply terminal and the output of the latch, and wherein the comparator also comprises a second negative feedback loop to set the second threshold of the comparator as a function of a second power supply potential and a second potential that is stable.

3. A comparator according to one of the claims 1 or 2, wherein the first threshold is a top threshold and wherein the first reference potential is chosen to be smaller than or equal to the first power supply potential, which is positive.

4. A comparator according to claim 3, wherein the first threshold is chosen such that the difference between the first power supply potential and the first reference potential is positive and

increasing as a function of the first power supply potential VDD to limit the increase in the value of the first threshold when the first power supply potential increases.

5. A comparator according to one of the claims 1 to 4, wherein the second threshold is a bottom threshold and wherein the second reference potential is chosen to be greater than or equal to the second power supply potential which is a ground potential.

6. A comparator according to one of the claims 1 to 5, wherein the first negative feedback loop comprises a first transistor, a source of which is connected to a first midpoint and a gate of which is connected to a source of a second transistor, a gate of which is connected to the output of the comparator, the first power supply potential being applied to the drain of the first transistor and the first reference potential being applied to the drain of the second transistor.

7. A comparator according to claim 6, wherein the first negative feedback loop also comprises a third transistor, a drain of which is connected to the gate of the first transistor, a gate of which is connected to the output of the comparator, the second power supply potential being applied to the source of the third transistor.

8. A comparator according to one of the claims 1 to 7, wherein the second negative feedback loop comprises a fourth transistor, a source of which

is connected to the second midpoint and a gate of which is connected to the source of a fifth transistor, a gate of which is connected to the output of the comparator, the second power supply potential being applied to the drain of the fourth transistor and the second reference potential being applied to the drain of the fifth transistor.

9. A comparator according to claim 8, wherein the second negative feedback loop also comprises a sixth transistor, a drain of which is connected to the gate of the fourth transistor and a gate of which is connected to the output of the comparator, the first power supply potential being applied to the drain of the sixth transistor.

10. A comparator according to one of the claims 1 to 9, wherein the two-threshold inverter comprises a seventh transistor and an eighth transistor of a first type, a ninth transistor and a tenth transistor of a second type series-connected between the first power supply terminal and the second power supply terminal, the gates of said transistors being connected together to the input terminal of the two-threshold inverter.

11. A comparator according to claim 10, wherein the two-threshold inverter also comprises a single inverter connected between, firstly, the common drain of the eighth transistor and the ninth transistor and, secondly, the output of the comparator.